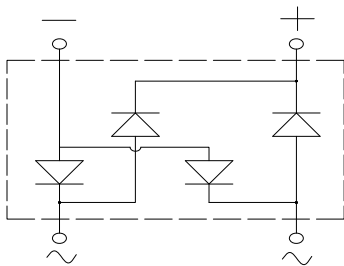
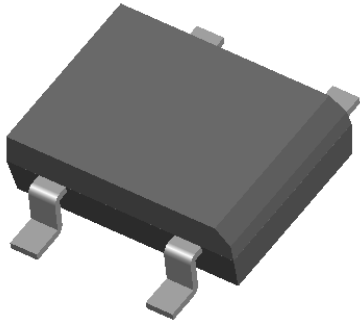


## Bridge Rectifiers



### Features

- UL recognition, file #E313149
- Glass passivated chip junction
- Ideal for automated placement
- High surge current capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

### Typical Applications

General purpose use in AC/DC bridge full wave rectification for SMPS, lighting ballast, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

### Mechanical Data

- **Package:** DBLS  
Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, Halogen free
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Polarity:** As marked on body

### ■ Maximum Ratings (T<sub>a</sub>=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	DBL101S	DBL102S	DBL103S	DBL104S	DBL105S	DBL106S	DBL107S
Device marking code			DBL101S	DBL102S	DBL103S	DBL104S	DBL105S	DBL106S	DBL107S
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	V	50	100	200	400	600	800	1000
Maximum RMS Voltage	V <sub>RMS</sub>	V	35	70	140	280	420	560	700
Maximum DC blocking Voltage	V <sub>DC</sub>	V	50	100	200	400	600	800	1000
Average rectified output current @60Hz sine wave, R-load, T <sub>c</sub> =134°C	I <sub>O</sub>	A	1.0						
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, T <sub>j</sub> =25°C	I <sub>FSM</sub>	A	30						
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, T <sub>j</sub> =25°C			60						
Current squared time @1ms ≤ t ≤ 8.3ms T <sub>j</sub> =25°C, Rating of per diode	I <sup>2</sup> t	A <sup>2</sup> s	3.7						
Storage temperature	T <sub>stg</sub>	°C	-55 ~ +150						
Junction temperature	T <sub>j</sub>	°C	-55 ~ +150						

### ■ Electrical Characteristics (T<sub>a</sub>=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	DBL101S	DBL102S	DBL103S	DBL104S	DBL105S	DBL106S	DBL107S
Maximum instantaneous forward voltage drop per diode	V <sub>F</sub>	V	I <sub>FM</sub> =0.5A	1.0						
Maximum DC reverse current at rated DC blocking voltage per diode	I <sub>R</sub>	μA	T <sub>j</sub> =25°C	5						
			T <sub>j</sub> =125°C	100						
Typical junction capacitance	C <sub>j</sub>	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	12						



# DBL101S THRU DBL107S

## ■ Thermal Characteristics (T<sub>a</sub>=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	DBL101S	DBL102S	DBL103S	DBL104S	DBL105S	DBL106S	DBL107S
Typical Thermal Resistance	R <sub>θJ-A</sub>	°C/W	40.0						
	R <sub>θJ-L</sub>		15.0						
	R <sub>θJ-C</sub>		8.0						

Note: Device mounted on P.C.B with 35mm\*25mm\*1.7mm

## ■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
DBL101S ~ DBL107S	B1	Approximate 0.32	50	5000	20000	TUBE
DBL101S ~ DBL107S	F1	Approximate 0.32	1500	3000	21000	REEL

## ■ Characteristics (Typical)

FIG1: I<sub>o</sub>-T<sub>c</sub> Curve

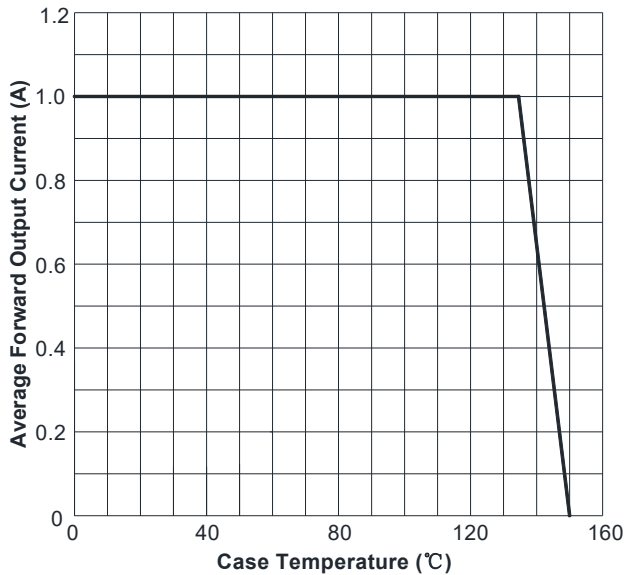


FIG2: Surge Forward Current Capability

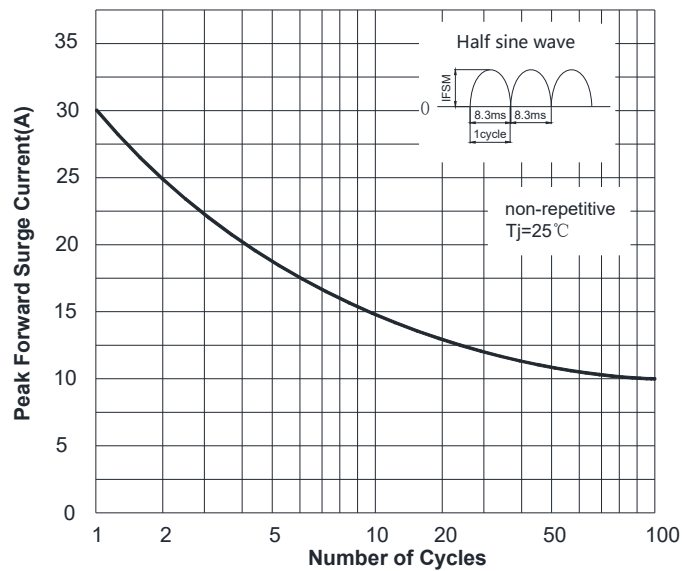


FIG3: Typical Forward Voltage

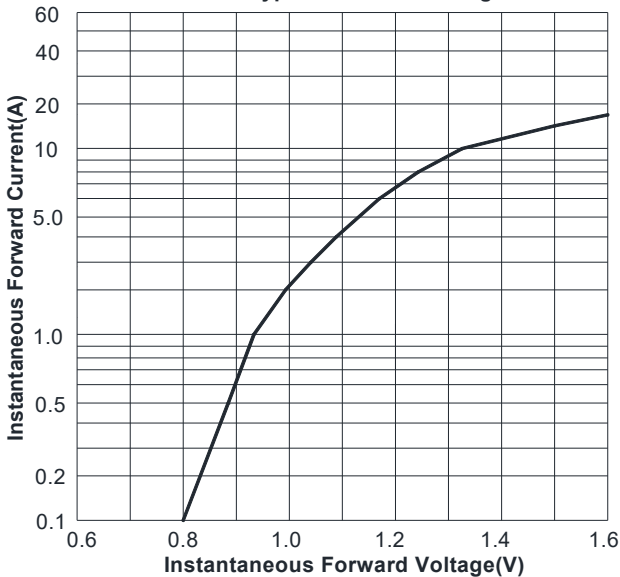
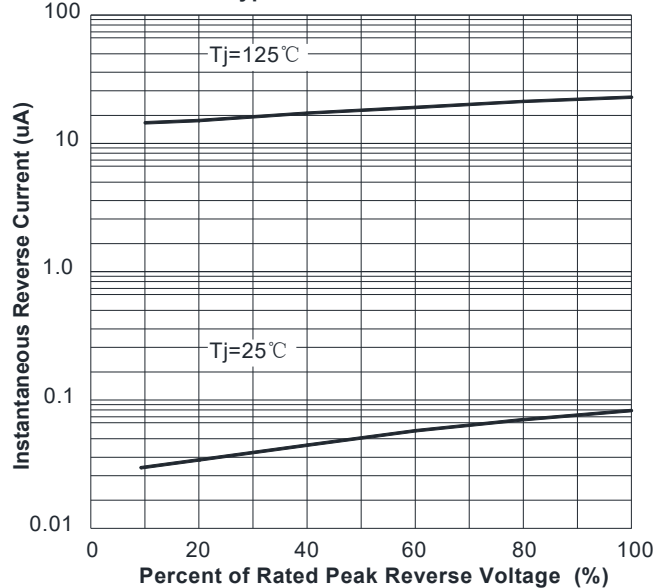


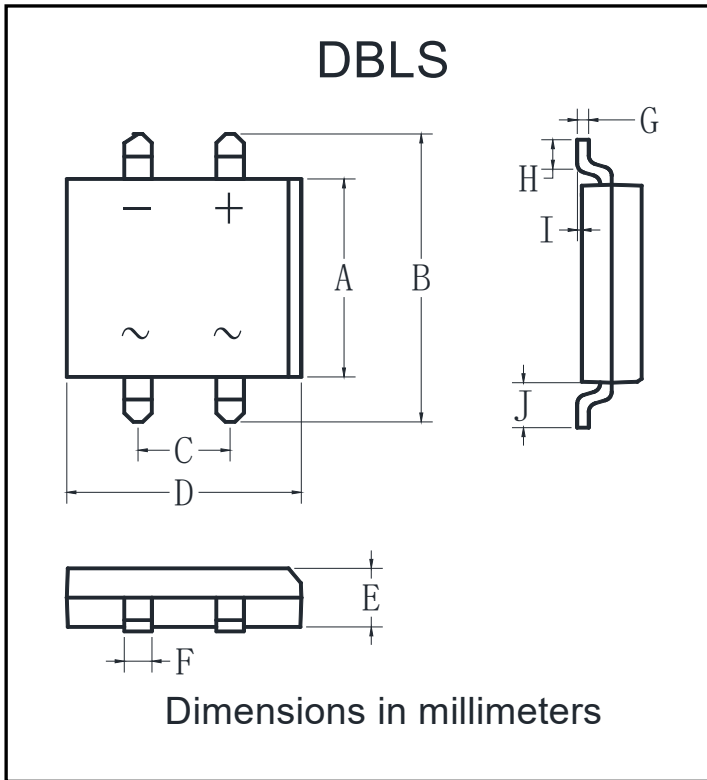
FIG4: Typical Reverse Characteristics





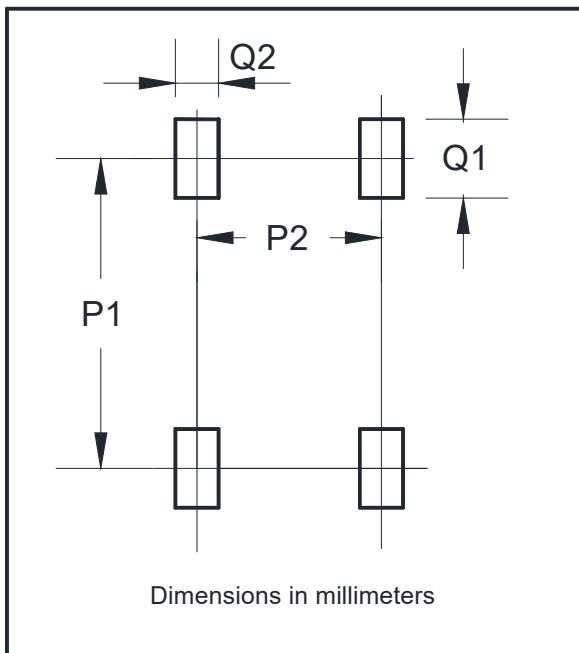
# DBL101S THRU DBL107S

## ■ Outline Dimensions



DBLS		
Dim	Min	Max
A	6.20	6.50
B	9.60	10.30
C	5.00	5.20
D	8.13	8.51
E	2.35	2.45
F	1.02	1.2
G	0.22	0.33
H	1.02	1.53
I	0	0.30
J	1.80	2.10

## ■ Suggested pad layout



Dim	Min
P1	8.73
P2	5.12
Q1	2.22
Q2	1.2



## DBL101S THRU DBL107S

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